PTO/SB/30 (08-00) 2823

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REQUEST FOR

## CONTINUED EXAMINATION (RCE) TRANSMITTAL

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995.

See The American Inventors Protection Act of 1999 (AIPA).

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|--|------------------------|--|--|
| Application Number   | 10/032,564             |  |  |
| Filing Date  | January 2, 2002        |  |  |
| First Named Inventor   | Yasutoshi OKUNO et al. |  |  |
| Group Art Unit   | 2823                   |  |  |
| Examiner Name  | Michelle Estrada       |  |  |
| Attorney Docket Number   | 740819-725             |  |  |

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

NOTE: 37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified application was filed prior to May 29, 2000, applicant may wish to consider filing a continued prosecution application (CPA) under 37 C.F.R. § 1.53(d) (PTO/SB/29) instead of a RCE to be eligible for the patent term adjustment provisions of the AIPA. See Changes to Application Examination and Provisional Application Practice, Final Rule, 65 Fed. Reg. 50092 (Aug. 16, 2000); Interim Rule, 65 Fed. Reg. 14865 (Mar. 20, 2000), 1233 Off. Gaz. Pat. Office 47 (Apr. 11, 2000), which established RCE practice.

| <u> </u>   |   |   |   |  |
|--|---|---|---|--|
| 1. Submission required under 37 C.F.R. § 1.114   | ľ   |   | JAN<br>2800   |  |
| a. Previously submitted  |   |   |   |  |
| 1. Submission required under 37 C.F.R. § 1.114  a. Previously submitted  i. Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on (Any unentered amendment(s) referred to above will be entered).  ii. Consider the arguments in the Appeal Brief or Reply Brief previously filed on iii. Other  b. Enclosed  i. Amendment/Reply |   |   |   |  |
| ii. Consider the arguments in the Appeal Brief or Reply Brief previously filed on  |   |   |   |  |
| iii. Dother  |   | · · · · · · · · · · · · · · · · · · ·   | IL PO   |  |
| b. Enclosed  |   |   | Q   |  |
| i.   |   |   |   |  |
| ii.  |   |   |   |  |
| iii.   | DS)   |   |   |  |
| iv. Other Preliminary Amendment  | -,  |   |   |  |
| 2. Miscellaneous   |   |   |   |  |
| 3. Fees The RCE fee under 37 C.F.R. § 1.17(e) is required a. The Director is hereby authorized to charge i. RCE fee required under 37 C.F.R. § 1 ii. Extension of time fee (37 C.F.R. §§ 1 iii. Other  b. Check in the amount of \$740.00 enclosed c. Payment by credit card (Form PTO-2038 enclosed)  | ired by 37 C.F.R. § 1.11<br>e the following fees,<br>.17(e)<br>.136 and 1.17) | us; Fee under 37 C.F.R. § 1.17(1)  4 when the RCE is filed.  or credit any overpayments, to | required)   |  |
| SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED  |   |   |   |  |
| Name (Print/Type) Donald R. Studebaker   |   | tion No. (Attorney/Agent)   | 32,815  |  |
| Signature DUN  |   | December 20, 2002   |   |  |
| CERTIFICATE OF MAILING OR TRANSMISSION   |   |   |   |  |
| hereby certify that this correspondence is being deposited on an envelope addressed to: Commissioner For Patents, Borrademark Office on:   |   |   | t postage as first class mail nitted to the U.S. Patent and |  |

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Date

December 20, 2002

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Name (Print/Type)

Signatur

-Jeannie Saxton

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Docket No. 740819-725 1/23/0

Seria No. 10/032,564

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE TC 2800 MAIL ROOM

In re Patent Application of:

Yasutoshi OKUNO et al. ) Group Art Unit: 2823

Serial No. 10/032,564 ) Examiner: Michelle Estrada

Filed: January 2, 2002

For: METHOD FOR FABRICATING ) Date: December 20, 2002

SEMICONDUCTOR DEVICE

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 2023 no December 20, 2002.

Jeannie Saxton

## PRELIMINARY AMENDMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

Please preliminarily amend the above-identified patent application as follows:

## IN THE CLAIMS:

Please amend claims 1, 3 and 8 as follows.

1. (Twice Amended) A method for fabricating a semiconductor device including a capacitor device having a lower electrode, a capacitor dielectric film formed on said lower electrode and an upper electrode formed on said capacitor dielectric film, comprising a step of:

forming a conducting film to be formed into said lower electrode including substeps of:

NVA250099.1